

CSC258: Computer Organization Winter 2017

This course provides an introduction to the underlying digital structures of computers. Topics include digital logic representation and design, computer system organization and microprogramming.

Instructor Information

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Course Information

Information pertaining to this course will be available off of Blackboard. The course website will have course announcements & materials, discussion boards, relevant readings, as well as assignment, lab & project details. Announcements will be made through the email registered on Blackboard, but the site is required reading, and it is understood that you will check it multiple times a week.

Course Textbooks

Required:	Mano, Kime, <i>Logic and Computer Design Fundamentals</i> , 4th ed., Prentice Hall, 2008
Recommended:	Hamacher, Vranesic, Zaky, <i>Computer Organization</i> , 5th ed., McGraw Hill, 2002 Null, Lobur, <i>The Essentials of Computer Organization and Architecture</i> , 3rd ed., Jones & Bartlett Publishing, 2012

Mark Breakdown

Component	Weight
Labs	28% (7 total, 4% each)
Project	14% (2% proposal and peer feedback, 3x4% demos)
Midterm exam	18% (closed-book)
Final exam	40% (also closed-book) → you must get 40% on the final to pass the course

• Labs:

- The labs consist of hands-on lab exercises that take place in BA3145, BA3155 & BA3165. Lab exercises must be completed and shown to the TA by the end of the session.
- Pre-lab reports are mandatory for each lab, and must be printed and handed in to the TA at the beginning of the lab session. Students who fail to do this will not be allowed to do the lab.
- Labs take place every week for two months, starting in the second week of class (see dates below).
- Tutorial sessions will be used to discuss the upcoming lab work.

• Project:

- A large design project for the last month of the course, created using the principles covered in labs and lectures. Marks are also given for successful implementation, innovative design and creativity.
- Project proposal document is worth 1%, and is submitted at the same time as the prelab for Lab 6.
- Peer feedback on other proposals is worth 1%, and takes place in the week after Lab 6.
- Project demos are performed in the lab, and are worth 4% each.

Students are **required** to work in pairs for the labs and the project.

Important Dates

Week	Topics	Milestone(s)
Jan 5 – 11	Overview, transistors, basic logic gates	
Jan 12 – 18	Combinational circuit design, K-maps	Lab 0
Jan 19 – 25	Logical devices (muxes, adders, decoders)	Lab 1
Jan 26 – Feb 1	Latches & flip-flops	Lab 2
Feb 2 – 8	Registers, counters, finite state machines	Lab 3
Feb 9 – 17	Finite state machine design, midterm review	Lab 4
Feb 20 -24	-- Reading Week --	
Feb 27 – Mar 1	Registers, memory (RAM & ROM)	Midterm exam*
Mar 2 – 8	Architecture & microprogramming	Lab 5
Mar 9 – 15	Assembly language basics	Lab 6 & Project proposal
Mar 16 – 22	Assembly language program design	Project demo #1
Mar 23 – 29	Advanced assembly language	Project demo #2
Mar 30 – Apr 5	Topic overflow & course review	Project demo #3 & project report

**The midterm will be on Feb 28, 7pm-9pm. Please report conflicts to your instructor by Feb 1.*

Lateness is generally not accepted, except in cases of medical emergency. Lateness due to personal reasons must be brought to the instructor for consideration, as early as possible.

Administrative Details

Plagiarism is very bad. Please don't do it. It just makes things unpleasant for everybody involved. In case you need clarification on the university's policies on plagiarism, please consult the *Code of Behaviour on Academic Matters* from this website: www.artsci.utoronto.ca/osai/students

Feedback on the course is solicited during end-of-term evaluations. However, feedback before that point is encouraged, to improve the delivery of the course. Please make sure your concerns are voiced to the course instructor or the teaching assistants whenever possible.